
Subject: Re: IDL for IRIX

Posted by [K. Bowman](#) on Mon, 13 Jan 2003 17:10:16 GMT

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In article <avuo4s\$fab\$1@south.jnrs.ja.net>,
Nigel Wade <nmw@ion.le.ac.uk> wrote:

> I've certainly never seen IDL use more than a single processor for internal
> operations. Is it supposed to?

Newer versions of IDL support multi-threading. See Chapter 15 in the
5.6 manuals "Multithreading in IDL". It's in the "What's New" manual in
5.5, I think. I can't remember in which version it first appeared.

Ken Bowman

On my dual-processor Mac:

IDL> help, !cpu, /str

** Structure !CPU, 6 tags, length=24, data length=24:

HW_VECTOR	LONG	1
VECTOR_ENABLE	LONG	1
HW_NCPU	LONG	2
TPOOL_NTHREADS	LONG	2
TPOOL_MIN_ELTS	LONG	100000
TPOOL_MAX_ELTS	LONG	0

This translates as:

Vector unit present (currently Mac only)

Vector unit enabled

2 cpus available

use 2 threads when vectors satisfy these conditions:

minimum vector length, 100000

no maximum vector length
