
Subject: Bus error

Posted by [Ed Wright](#) on Fri, 09 Jul 2004 19:42:28 GMT

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Question, what memory conditions can cause a Bus error after a
.full_session_reset?

Example:

```
IDL> cell1 = ICY_CELL_DBL(10)
% Loaded DLM: ICY.
IDL> .full_session_reset
IDL> cell1 = ICY_CELL_DBL(10)
% Loaded DLM: ICY.
IDL> .full_session_reset
IDL> cell1 = ICY_CELL_DBL(10)
% Loaded DLM: ICY.
Bus error
```

As always,
Ed Wright

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