Subject: Re: Possible Mac OS Bug with FIX.
Posted by savoie on Wed, 11 May 2005 20:14:41 GMT
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David Fanning <davidf@dfanning.com> writes:

> What I am still not clear about, however, is whether whatever
> the hell it is that RSI is doing is done *consistently*
> across all machine architectures. Anyone have a theory about
> that? :-)

It seems pretty clear to me what's happening is what lajos previously stated. But the more I tried to articulate that, the less sense it made. Anyway, on my two machines,

Linux little endian:

```
IDL> tmp = bytarr(20)
tmp = bytarr(20)
IDL> tmp[15] = 5
tmp[15] = 5
IDL> print, fix(tmp[15]), fix(tmp,15)
print, fix(tmp[15]), fix(tmp,15)
      5       5
```

On a Big Endian IRIX64 machine

```
IDL> tmp=bytarr(20)
IDL> tmp[15] = 5
IDL> print, fix(tmp[15]), fix(tmp,15)
      5    1280
```

I don't even know if this helps.

Matt

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Matthew Savoie - Scientific Programmer
National Snow and Ice Data Center
(303) 735-0785 http://nsidc.org